

Substitute for form 1449A/PTO		Application Number	10716,596
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>		Filing Date	November 20, 2003
		First Named Inventor	Michael Farnwald
		Art Unit	2111
		Examiner Name	Glenn Allen Auve
Sheet	5 of 18	Attorney Docket Number	57941.000063

**U.S. PATENT DOCUMENTS**

Examiner Initials	Case No.	DOCUMENT NUMBER Number - Kind Code (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
GA	93.	US- 4,933,835	06-12-1990	Sachs, et al.	
	94.	US- 4,937,733	06-26-1990	Gillett, Jr., et al.	
	95.	US- 4,939,510	07-03-1990	Masheff, et al.	
	96.	US- 4,940,909	07-10-1990	Mulder, et al.	
	97.	US- 4,945,471	07-31-1990	Neches	
	98.	US- 4,945,516	07-31-1990	Kashiyama	
	99.	US- 4,947,484	08-07-1990	Twitty, et al.	
	100.	US- 4,949,301	08-14-1990	Joshi, et al.	
	101.	US- 4,954,992	09-04-1990	Kumanoya, et al.	
	102.	US- 4,965,792	10-23-1990	Yano	
	103.	US- 4,975,763	12-04-1990	Baudouin, et al.	
	104.	US- 4,982,400	01-01-1991	Ebersole	
	105.	US- 4,998,069	03-05-1991	Nguyen, et al.	
	106.	US- 5,009,481	04-23-1991	Kinoshita, et al.	
	107.	US- 5,012,408	04-30-1991	Conroy	
	108.	US- 5,023,488	06-11-1991	Gunning	
	109.	US- 5,034,964	07-23-1991	Khan, et al.	
	110.	US- 5,038,317	08-06-1991	Callan, et al.	
	111.	US- 5,038,320	08-06-1991	Heath, et al.	
	112.	US- 5,056,060	10-08-1991	Fitch, et al.	
▼	113.	US- 5,063,564	11-05-1991	Crandall, et al.	
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GA	115.	US- 4,941,128	07-10-1990	Wada, et al.	

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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>	PTO-1449 (Modified)	PROPERTY. DOCKET NO. 57941.000063	SERIAL NUMBER 10/716,596
	APPLICANT(S) MICHAEL FARMWALD ET AL.		
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EXAMINER INITIALS		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
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GA	226.	WO 89/12936	12/28/89	PCT			
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	231.	Schnaitter, William M. et al., "A 0.5-GHz CMOS Digital RF Memory Chip," IEEE Journal of Solid-State Circuits, vol. SC-21, no. 5, pp. 720-726 (Oct. 1986)
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	137.	4,916,670	04/10/90	Suzuki et al.			
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	76.	F. Towler et. al., "A 128k 6.5ns Access/ 5ns Cycle CMOS ECL Static RAM", 1989 IEEE International Solid State Circuits Conference, (Feb. 1989)
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	7.	"LR2010 Floating Point Accelerator Preliminary" LSI Logic Corp. 1988, pp 1-20
	8.	"High Speed CMOS Databook", Integrated Device Technology Inc. Santa Clara, CA, 1988 pp 9-1 to 9-14
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GA	10.	Moussouris, J. "The Advanced Systems Outlook-Life Beyond RISC: The next 30 years in high-performance computing", Computer Letter, July 31, 1989 (an edited excerpt from an address at the fourth annual conference on the Advanced Systems Outlook, in San Francisco, CA (June 5))

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